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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/043,803	01/10/2002	Richard Clewer	PD-201003	9080
75	590 04/03/2006		EXAMINER	
Hughes Electronics Corporation			FLANAGAN, KRISTA M	
Patent Docket Administration Bldg. 1, Mail Stop A109			ART UNIT	PAPER NUMBER
P.O. Box 956	•		2817 DATE MAILED: 04/03/2006	
El Segundo, Ca	A 90245-0956			

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	Ü
	10/043,803	CLEWER ET AL.	
Office Action Summary	Examiner	. Art Unit	
	Krista M. Flanagan	2817	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet	vith the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REWHICHEVER IS LONGER, FROM THE MAILING Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory per Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	B DATE OF THIS COMMUN R 1.136(a). In no event, however, may liod will apply and will expire SIX (6) MO atute, cause the application to become	ICATION. I reply be timely filed INTHS from the mailing date of this communication. ABANDONED (35 U.S.C. § 133).	٠
Status			
1) Responsive to communication(s) filed on 10) January 2006.		
2a) ☐ This action is FINAL 2b) ☒ T	his action is non-final.	•	
3) Since this application is in condition for allow	•	• •	
closed in accordance with the practice unde	er <i>Ex parte Quayle</i> , 1935 C.	D. 11, 453 O.G. 213.	
Disposition of Claims			
4) ⊠¹ Claim(s) 1-20 is/are pending in the application 4a) Of the above claim(s) is/are without 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-20 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and	Irawn from consideration.		
Application Papers		- 0	
9) The specification is objected to by the Exam 10) The drawing(s) filed on is/are: a) a Applicant may not request that any objection to t Replacement drawing sheet(s) including the corr 11) The oath or declaration is objected to by the	accepted or b) objected to he drawing(s) be held in abeya rection is required if the drawin	ince. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119		•	
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the papplication from the International Burn * See the attached detailed Office action for a least company to the certified copies of the papplication from the International Burn * See the attached detailed Office action for a least copies.	ents have been received. ents have been received in riority documents have bee eau (PCT Rule 17.2(a)).	Application No n received in this National Stage	
Attachment(s)			
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) 		Summary (PTO-413) (s)/Mail Date	
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/ Paper No(s)/Mail Date		Informal Patent Application (PTO-152)	

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DETAILED ACTION

Response to Arguments

- 1. Applicant's arguments filed 10 January 2006 have been fully considered but they are not persuasive. Applicant argues that Genrich does not disclose two critical features recited in independent claim 1 and 11 as amended by the Response filed on 15 April 2005: 1) a demodulator and a method of demodulating capable of partitioning an incoming modulation signal having data rate R using N shift registers; and 2) a demodulator and a method of demodulating that used N shift registers having a date rate dependent on the number of shift registers being used to process the incoming modulated signal i.e., R/N. Examiner ages that Genrich does not explicitly disclose a demodulator having N shift registers. However, Genrich does use a demultiplexer to partition the signal and it is well known in the art that a demultiplexer can use shift registers in its architecture.
- 2. Applicant's arguments with respect to claims 1-20 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

- 3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 4. Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Genrich, US Patent No. 6,055,280 in further view of Negus, US Patent No. 5,150,364.
- 5. Genrich discloses a high data rate digital demodulator and bit synchronizer for intended use in high data rate ground station applications. A high data rate digital BPSK/QPSK demodulator (10) analog to digital converter section (20) comprises a demultiplexer (14), which

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outputs a clock and a data signal at that lower/divided clock rate is disclosed (See figure 1 and corresponding text, column 2, lines 1-4 and 30-32, column 6, lines 38-41 and column 7, lines 1-4). A digital signal processor (30) then receives the clock and the data signal and processes the demultiplexed signal and outputs the detected signals from the demodulator (See figure 2 and corresponding text). Genrich does not explicitly disclose a demultiplexer circuit having shift registers. However, Negus discloses an interleaved time division demultiplexer having latches. The demultiplexer of Negus demultiplexes a serial input bit stream having an input data rate of B into M parallel output bits and also yields a clock frequency of B/M (See abstract). It would be obvious for one of ordinary skill in the art to use the demultiplexer of Negus in the demodulator of Genrich without any change in performance.

Conclusion

- 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Krista M. Flanagan whose telephone number is (571) 272-2203.

 The examiner can normally be reached on Monday Friday, 8 4:30.
- 7. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert J. Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.
- 8. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

K. Flanagan 20060329

Bobert Pascal Supervisory Patent Examiner Technology Center 2800